Patent

Attorney's Docket No.: P18395

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Patent Application of:

Dennis M. O'Connor, et al.

Examiner:

Application No.:

Art Unit:

Filed:

Title:

METHOD AND SYSTEM TO ORDER

**MEMORY OPERATIONS** 

## INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

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Sir:

In accordance with the duty of disclosure, enclosed is a copy of Information Disclosure Statement by Applicant (form PTO/SB/08), which is being submitted concurrently with the Utility Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Respectfully submitted,

Date: March 10, 2004

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PTO/SB/08B (08-03)

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	Application Number	
INFORMATION DISCLOSURE	Filing Date	
STATEMENT BY APPLICANT	First Named Inventor	Dennis M. O'Connor
	Art Unit	
(Use as many sheets as necessary)	Examiner Name	
Sheet 1 of 1	Attorney Docket Number	P18395

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
	1	JOHN L. HENNESSY, ET AL., "Computer Architecture", A Quantitative Approach, 3rd Addition 2003, Pgs. 531, 549, 550-551, 605-608.		
	2	K. GHARACHORLOO, ET AL., "Memory Consistency and Event Ordering In Scalable Shared-Menory Multiprocessors, 1990 IEEE, Pgs. 15-26.		
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